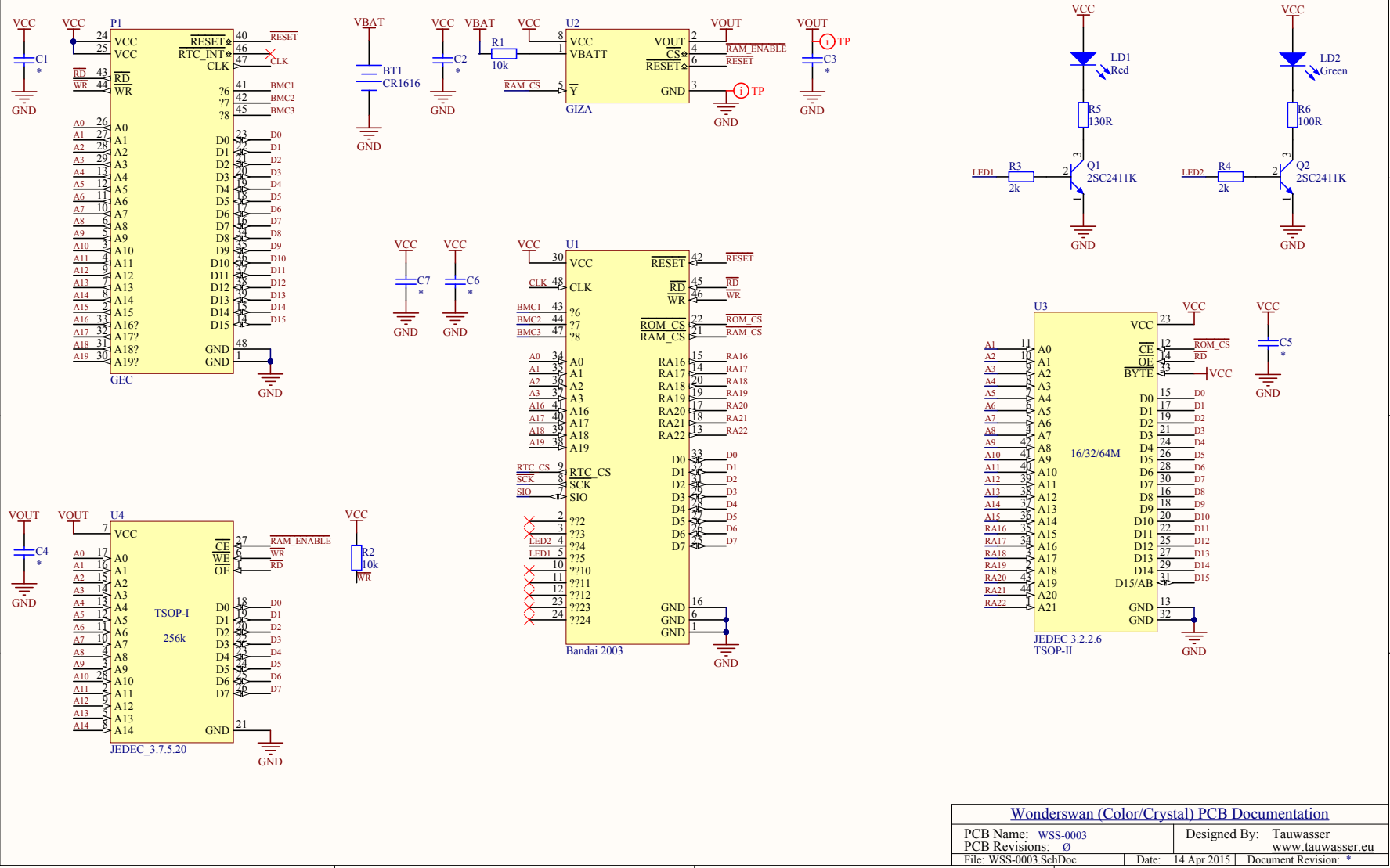
C1
*
GND
VCC
P1
VCC
VCC
RESET
RTC_INT
CLK
RD
WR
?6
?7
?8
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
A16?
A17?
A18?
A19?
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15
GND
GEC
RESET
CLK
BMC1
BMC2
BMC3
VBAT
BT1
CR1616
C2
R1
10k
RAM_CS
U2
VCC
VBATT
VOUT
CS
RESET
GND
GIZA
RAM_ENABLE
TP
C3
LD1
Red
R5
130R
R3
2k
LED1
Q1
2SC2411K
LD2
Green
R6
100R
R4
2k
LED2
Q2
2SC2411K
C7
C6
U1
CLK
RD
WR
ROM_CS
RAM_CS
RTC_CS
SCK
SIO
LED2
LED1
RA16
RA17
RA18
RA19
RA20
RA21
RA22
Bandai 2003
U3
CE
OE
BYTE
16/32/64M
JEDEC 3.2.2.6
TSOP-II
C5
C4
U4
WE
TSOP-I
256k
JEDEC_3.7.5.20
R2
10k
Wonderswan (Color/Crystal) PCB Documentation
PCB Name: WSS-0003
PCB Revisions: Ø
Designed By: Tauwasser
www.tauwasser.eu
File: WSS-0003.SchDoc
Date: 14 Apr 2015
Document Revision: *